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ATTY. DOCKET NO.
1778.0200000

FIRST NAMED INVENTOR
Ryan C. Kinter

FILING DATE
April 18, 2001

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APPLICATION NO.
09/836,541

			U.S. PA	TENT DOCUMENTS					
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE		
	AA1	3,631,405	12/1971	Hoff et al.	02.00	05.00	T IEII O O/IIE		
2	AB1	3,794,980	02/1974	Cogar et al.					
N	AC1	3,811,114	05/1974	Lemay et al.					
h	AD1	3,840,861	10/1974	Amdahl et al.					
N	AE1	3,983,541	09/1976	Faber et al.		-			
W	AF1	4,110,822	08/1978	Porter et al.		- 			
h	AG1	4,149,244	04/1979	Anderson et al.					
h	AH1	4,229,790	10/1980	Gilliland et al.	-				
<u> </u>	Al1	4,295,193	10/1981	Pomerene, James H.					
7	AJ1	4,432,056	02/1984	Aimura, Harutsugu		1	<u> </u>		
	1			PATENT DOCUMENTS			<u> </u>		
EXAMINER		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION		
INITIAL	AK1	EP 0 073 424 A2	03/1983	Europe	02400	CLASS	N/A		
		2, 00,042472		Саторо		 			
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		OTHER (In	cluding Auth	or, Title, Date, Pertinent	Pages, etc.)				
, ,		·	···			·			
	AO1	U.S. Reissue Patent Application No. 10/066,475, inventor Edward Colles Nevill, filed February 1, 2002 (based on U.S. Pat. No. 6,021,265, issued February 1, 2000) (9 pages).							
a	AP1	Preliminary Amendment, filed February 1, 2002, in U.S. Reissue Patent Application No. 10/066,475, inventor Edward Colles Nevill, filed February 1, 2002 (based on U.S. Pat. No. 6,021,265, issued February 1, 2000) (15 pages).							
0	AQ1	Case, Brian, "ARM Architecture Offers High Code Density: Non-Traditional RISC Encodes Many Options in Each Instruction," <i>Microprocessor Report</i> , Vol. 5, No. 23, pgs. 11-14 (December 18, 1991).							
EXAMINER			7		DATE	ONSTOERE			
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nd not conside	ered. Indi	ude copy of this form with	next communicat	tion to Applicant.	-////	-//			

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ART UNIT
2183

EXAMINER			U.S. PA	ATENT DOCUMENTS			
INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
<u> </u>	AA2	4,467,409	08/1984	Potash et al.	02.00		TIENTO DI TIE
6	AB2	4,507,728	03/1985	Sakamoto et al.			
//	AC2	4,685,080	08/1987	Rhodes, Jr. et al.			
/ /	AD2	4,724,517	02/1988	May, Michael D.			
5	AE2	4,777,594	10/1988	Jones et al.			-
2	AF2	4,782,441	11/1988	Inagami et al.			
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0	AM2	EP 0 368 332 B1	09/1997	Europe			N/A
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4	AN2	Cobb, Paul, "TinyR 13 slides (7 pages)		16 embedded CPU core," P 23, 1996).	resentation	for Micropro	cessor Forum,
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		Gwennap, Linley, "Cheaper Than RISC Kurosawa, K., et al.	VLIW: The WC," Microproce	23, 1996). Vave of the Future?: Proces	sor Design (pp. 18-21 (Formance In	Style Could February 14 Itegrated Pr	Be Faster, , 1994). olog Processor
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	AO2	Gwennap, Linley, "Cheaper Than RISC Kurosawa, K., et al. IPP," Logic Program (August 15-19, 198) NEC Data Sheet, M. NEC Electronics Co.	VLIW: The WC," Microproce , "Instruction mming: Proce 8), MIT Press IOS Integrated orporation 200	Zave of the Future?: Processessor Report, Vol. 8, No. 2, Architecture For A High Perendings of the Fifth Internat, Cambridge, MA, Vol. 2, pp. d Circuit, uPD30121, VR41200) (76 pages).	ssor Design (Fig. 18-21 (Fig.	Style Could February 14 Itegrated Prence and S (1988).	Be Faster, , 1994). olog Processor ymposium essor (Copyright
	AO2	Gwennap, Linley, "Cheaper Than RISC Kurosawa, K., et al. IPP," Logic Progra. (August 15-19, 198 NEC Data Sheet, M. NEC Electronics Co. NEC User's Manual	VLIW: The WC," Microproce , "Instruction mming: Proce 8), MIT Press IOS Integrated orporation 200	Zave of the Future?: Processessor Report, Vol. 8, No. 2, Architecture For A High Perendings of the Fifth Internat, Cambridge, MA, Vol. 2, pp. d Circuit, uPD30121, VR41200) (76 pages).	sor Design 3 pp. 18-21 (F	Style Could February 14 Itegrated Prence and S (1988).	Be Faster, , 1994). colog Processor cymposium essor (Copyright

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INITIAL	AA3	5,506,974	DATE 04/1996	NAME Church et al.	CLASS	CLASS	TILING DATE			
51	AB3	5,574,873	11/1996	Davidian, Gary G.						
as	AC3	5,732,234	03/1998	Vassiliadis et al.						
	AD3	5,740,461	04/1998	Jaggar, David Vivian						
W	AE3	6,021,265	02/2000	Nevill, Edward Colles						
100	AF3	6,266,765 B1	07/2001	Horst, Robert W.			07/07/2000			
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	AJ3									
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EXAMINER		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION			
INITIAL	AK3	EP 0 449 661 B1	11/1995	Europe			N/A			
	AL3	GB 2 016 755 A	09/1979	United Kingdom			N/A			
	AM3						Yes No			
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6	AN3	NEC User's Manual, VR4121 TM , 64/32-Bit Microprocessor, uPD30121, pp. 1-19 and 103-131 (Chapter 4) (Copyright NEC Corporation 1998).								
2	A03	Ross, Roger, "There's no risk in the future for RISC," <i>Computer Design</i> , Vol. 28, No. 22, pp. 73-75 (November 13, 1989).								
	AP3			•						
	AQ3									
	AR3		7							
EXAMINER DATE CONSIDERED										
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.										